



#11/Suppl. Amend 0  
D/King 2819  
9/10/02 #

XA-9472  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.: 09/855,660

Group Art Unit: 2819

Filed: May 16, 2001

Examiner: D. Chang

For: SEMICONDUCTOR INTEGRATED CIRCUIT

\* \* \*

SUPPLEMENTAL AMENDMENT

Commissioner for Patents  
Washington D. C. 20231

Sir:

Supplementing the Amendment dated August 12, 2002,  
please further amend the above-identified patent  
application as indicated below.

09/07/2002 NHOHAKM1 00000043 09855660

342.00 OP  
162.00 OP

02/27/2003 TBELL1  
Adjustment date 02/27/2003 TBELL1  
09/03/2002 NHOHAKM1 00000043 09855660  
01 FC:1202  
02 FC:1202  
420.00 OP  
90.00 OP